

15/15



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Adrian E. Ong
Title: Chip Testing Within a Multi-Chip Semiconductor Package
Application No.: 10/824,734
Filing Date: April 15, 2004
Examiner: Dipakkumar B. Ganhdi
Group Art Unit: 2138
Confirmation No.: 6737
Law Office: Sidley Austin Brown & Wood LLP
Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

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A form PTO-1449 listing these documents is enclosed.

Citation of the above documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made; or

3. an admission that the information listed herein is, or is considered to be, material to patentability as defined in § 1.56(b).

This information disclosure statement is submitted under 37 C.F.R. § 1.97(e). In accordance with § 1.97(e)(2), no item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, no item of information contained in this information disclosure statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of this information disclosure statement. This certification does not extend to any items previously cited in this application.

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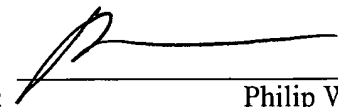
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Respectfully submitted,

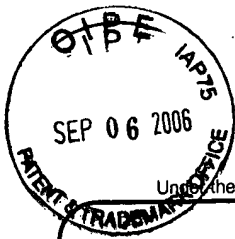
By:



Philip W. Woo
Attorney of Record
Reg. No. 39,880
PWW/rp

August 31, 2006

SIDLEY AUSTIN BROWN & WOOD LLP
555 California Street, Suite 2000
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Application Number

10/824,734

Filing Date

April 15, 2004

First Named Inventor

Adrian E. Ong

Art Unit

2138

Examiner Name

Dipakkumar B. Gandhi

Attorney Docket Number

24295/81003

ENCLOSURES (Check all that apply)

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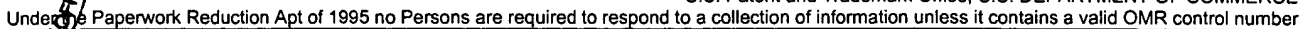
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Sheet 1 of 1

Application Number	10/824,734
Filing Date	April 15, 2004
First Named Inventor	Adrian E. Ong
Art Unit	2138
Examiner Name	Dipakkumar B. Gandhi
Attorney Docket Number	24295/81003

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Examiner
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Date
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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